



800 Series Microcontrollers

LM3S801

The **LM3S801** microcontroller is based on the ARM Cortex-M3 controller core, with 64 kB single-cycle flash, 8 kB single-cycle SRAM, low drop-out voltage regulator, brown-out reset and power-on reset controller, three 32-bit or six 16-bit timers, watchdog timer, Synchronous Serial Interface (SSI), I²C, three analog comparators, two UARTs, six motion-control Pulse Width Modulation (PWM) outputs, quadrature encoder input, and up to 36 GPIOs.

LM3S811

The **LM3S811** microcontroller is based on the ARM Cortex-M3 controller core, with 64 kB single-cycle flash, 8 kB single-cycle SRAM, low drop-out voltage regulator, brown-out reset and power-on reset controller, three 32-bit or six 16-bit timers, watchdog timer, Synchronous Serial Interface (SSI), I2C, analog comparator, two UARTs, 10-bit analog-to-digital converter (ADC) with four input channels, six motion-control Pulse Width Modulation (PWM) outputs, and up to 32 GPIOs.

LM3S812

The **LM3S812** microcontroller is based on the ARM Cortex-M3 controller core, with 64 kB single-cycle flash, 8 kB single-cycle SRAM, low drop-out voltage regulator, brown-out reset and power-on reset controller, three 32-bit or six 16-bit timers, watchdog timer, Synchronous Serial Interface (SSI), I2C, analog comparator, two UARTs, 10-bit analog-to-digital converter (ADC) with two input channels, two motion-control Pulse Width Modulation (PWM) outputs, and up to 34 GPIOs.

LM3S815

The **LM3S815** microcontroller is based on the ARM Cortex-M3 controller core, with 64 kB single-cycle flash, 8 kB single-cycle SRAM, low drop-out voltage regulator, brown-out reset and power-on reset controller, three 32-bit or six 16-bit timers, watchdog timer, Synchronous Serial Interface (SSI), I2C, three analog comparators, two UARTs, 10-bit analog-to-digital converter (ADC) with

two input channels, six motion-control Pulse Width Modulation (PWM) outputs, and up to 34 GPIOs.

LM3S817

The **LM3S817** microcontroller is based on the ARM Cortex-M3 controller core, with 64 kB single-cycle flash, 8 kB single-cycle SRAM, low drop-out voltage regulator, brown-out reset and power-on reset controller, three 32-bit or six 16-bit timers, watchdog timer, Synchronous Serial Interface (SSI), analog comparator, two UARTs, 10-bit analog-to-digital converter (ADC) with six input channels, six motion-control Pulse Width Modulation (PWM) outputs, and up to 30 GPIOs.

LM3S818

The **LM3S818** microcontroller is based on the ARM Cortex-M3 controller core, with 64 kB single-cycle flash, 8 kB single-cycle SRAM, low drop-out voltage regulator, brown-out reset and power-on reset controller, three 32-bit or six 16-bit timers, watchdog timer, Synchronous Serial Interface (SSI), three analog comparators, two UARTs, 10-bit analog-to-digital converter (ADC) with six input channels, six motion-control Pulse Width Modulation (PWM) outputs, and up to 30 GPIOs.

LM3S828

The **LM3S828** microcontroller is based on the ARM Cortex-M3 controller core, with 64 kB single-cycle flash, 8 kB single-cycle SRAM, low drop-out voltage regulator, brown-out reset and power-on reset controller, three 32-bit or six 16-bit timers, watchdog timer, Synchronous Serial Interface (SSI), I2C, two UARTs, 10-bit 1M samples per second analog-to-digital converter (ADC) with eight input channels, and up to 28 GPIOs.