



PhaseLink Corporation is the recognized leader in the mixed signal technology for timing source and signal conditioning ICs, in the area of Communications, Data Storage, Personal Computing, and Consumer Products. An innovator of many first to market products such as Analog Frequency Multipliers, Triangular Modulation SST, PicoPLL (world's smallest programmable clock), SMART-BYTETM Programmable clock chip, etc., PhaseLink offers one of the broadest IC product lines in the industry (XO, VCXO, AFM, PhasorV, EMI reduction, programmable, Video, and LAN clocks).

Product Family Summary

| Part Number | Input (MHz) | Output (MHz) | Operating Voltage | Features | Package |
|----------------------|--|--------------|-------------------|---|--|
| PL671-01 | <ul style="list-style-type: none"> Fundamental: 10 – 40 Reference: <200 | <200 | 2.5V 3.3V | <ul style="list-style-type: none"> Up to 3 programmable clocks. 4 on-the-fly configuration switching Programmable Modulation Rate, PDB or CLK, and output drive strength Center or down spread with 0.25% resolution | Wafer SOT23-6L MSOP-8L SOP-8L |
| PL671-03 | <ul style="list-style-type: none"> Fundamental: 10 – 40 | <200 | 2.5V 3.3V | <ul style="list-style-type: none"> Fixed multiplier of 2 or 4 4 pre-programmed Modulation Rates Output Enable (OE) | Wafer |
| PL671-05 PL671-06 | <ul style="list-style-type: none"> Reference: 10 - 200 | <200 | 2.5V 3.3V | <ul style="list-style-type: none"> Switchable input/output pins 4 on-the-fly configuration switching Center or down spread with 0.25% resolution | SOT23-6L |
| PL671-21 PL671-22 | <ul style="list-style-type: none"> Fundamental: 10 – 40 Reference: <200 | <200 | 2.5V 3.3V | <ul style="list-style-type: none"> Up to 2 programmable clocks. 2 on-the-fly configuration switching (-22) Programmable Modulation Rate, PDB or CLK, and output drive strength Center or down spread with 0.25% resolution | SOT23-6L |
| PL671-25 | <ul style="list-style-type: none"> Fundamental: 10 – 40 Reference: <200 | <200 | 2.5V 3.3V | <ul style="list-style-type: none"> Up to 3 programmable clocks. 4 on-the-fly configuration switching Programmable Modulation Rate, PDB or CLK, and output drive strength Center or down spread with 0.25% resolution | SOT23-6L MSOP-8L SOP-8L |