The XMEGA Product Range

All AVR XMEGA are pin and 100% code-compatibility across all devices from the smallest to the largest. It is possible to development with any XMEGA device, and switch to any other XMEGA device later without having to change any code. It enables companies to keep and maintain only one code base and use and re-use this across multiple projects. The result is a much faster development and prototyping cycles.

- Operating voltage form 1.6 to 3.6V
- CPU speed up to 32 MHz
- 16 to 384 Kbytes of Internal Flash Memory
- 44 to 100 pin packages
- 100% code compatibility
- Pin compatibility
- Easy to migrate between XMEGA devices

Product (a)	Status (b)	Flash (KB)	Boot code (Bytes)	EEPROM (KB)	SRAM (KB)	DMA (channels)	Event (Channels)	I/O pins	16-bit Timers	PWM (channel)	RTC 16-bit	RTC 32-bit (d)	SPI	TWI (I2C)	USART	12-bit ADC (ch.)	12-bit DAC (ch.)	Analog Comp.	Interrupts	Interrupts Ext.	Vcc Range (V)	Clock Speed (MHz)	Package (c)
ATxmega64A1	I	64	4	2	4	4	8	78	8	24	Y		4	4	8	2x8	2x2	4	122	78	1.6 - 3.6	32	TQFP100, BGA100
ATxmega128A1	I	128	8	2	8	4	8	78	8	24	Y		4	4	8	2x8	2x2	4	122	78	1.6 - 3.6	32	TQFP100, BGA100
ATxmega192A1	I	192	8	4	16	4	8	78	8	24	Y		4	4	8	2x8	2x2	4	122	78	1.6 - 3.6	32	TQFP100, BGA100
ATxmega256A1	I	256	8	4	16	4	8	78	8	24	Y		4	4	8	2x8	2x2	4	122	78	1.6 - 3.6	32	TQFP100, BGA100
ATxmega384A1	I	384	8	4	32	4	8	78	8	24	Y		4	4	8	2x8	2x2	4	122	78	1.6 - 3.6	32	TQFP100, BGA100
ATxmega64A3	I	64	4	2	4	4	8	50	7	22	Y		3	2	7	2x8	1x2	4	102	50	1.6 - 3.6	32	TQFP64, QFN64
ATxmega128A3	I	128	8	2	8	4	8	50	7	22	Y		3	2	7	2x8	1x2	4	102	50	1.6 - 3.6	32	TQFP64, QFN64
ATxmega192A3	I	192	8	4	16	4	8	50	7	22	Y		3	2	7	2x8	1x2	4	102	50	1.6 - 3.6	32	TQFP64, QFN64
ATxmega256A3B	I	256	8	4	16	4	8	49	7	22		Y	2	2	6	2x8	1x2	4	102	49	1.6 - 3.6	32	TQFP64, QFN64
ATxmega256A3	I	256	8	4	16	4	8	50	7	22	Y		3	2	7	2x8	1x2	4	102	50	1.6 - 3.6	32	TQFP64, QFN64
ATxmega16A4	F	16	4	1	2	4	8	34	5	16	Y		2	2	5	1x12	1x2	2	77	34	1.6 - 3.6	32	TQFP44, QFN44
ATxmega32A4	F	32	4	1	4	4	8	34	5	16	Y		2	2	5	1x12	1x2	2	77	34	1.6 - 3.6	32	TQFP44, QFN44
ATxmega64A4	F	64	4	2	4	4	8	34	5	16	Y		2	2	5	1x12	1x2	2	77	34	1.6 - 3.6	32	TQFP44, QFN44
ATxmega128A4	F	128	4	2	8	4	8	34	5	16	Y		2	2	5	1x12	1x2	2	77	34	1.6 - 3.6	32	TQFP44, QFN44
ATxmega64D3	F	64	4	2	4		4	50	5	18	Y		2	1	3	1x16		2	67	50	1.6 - 3.6	32	TQFP64, QFN64
ATxmega128D3	F	128	8	2	8		4	50	5	18	Y		2	1	3	1x16		2	67	50	1.6 - 3.6	32	TQFP64, QFN64
ATxmega192D3	F	192	8	2	16		4	50	5	18	Y		2	1	3	1x16		2	67	50	1.6 - 3.6	32	TQFP64, QFN64
ATxmega256D3	F	256	8	4	16		4	50	5	18	Y		2	1	3	1x16		2	67	50	1.6 - 3.6	32	TQFP64, QFN64
ATxmega16D4	F	16	4	1	2		4	34	4	14	Y		2	1	2	1x12		2	55	34	1.6 - 3.6	32	TQFP44, QFN44
ATxmega32D4	F	32	4	1	4		4	34	4	14	Y		2	1	2	1x12		2	55	34	1.6 - 3.6	32	TQFP44, QFN44

a) All XMEGA devices have Event System channels, and temperature range from -40°C to +85°C and 32 MHz, 2 MHz and 32 KHz calibrated PC oscillators.
b) F: Future product, I: Device under Introduction.
c) Pb-free packaging alternative, complies to the European Directive for Restriction of Hazardous Substances (RoHS directive). Also Halide free and fully Green.
d) Include Battery backup function

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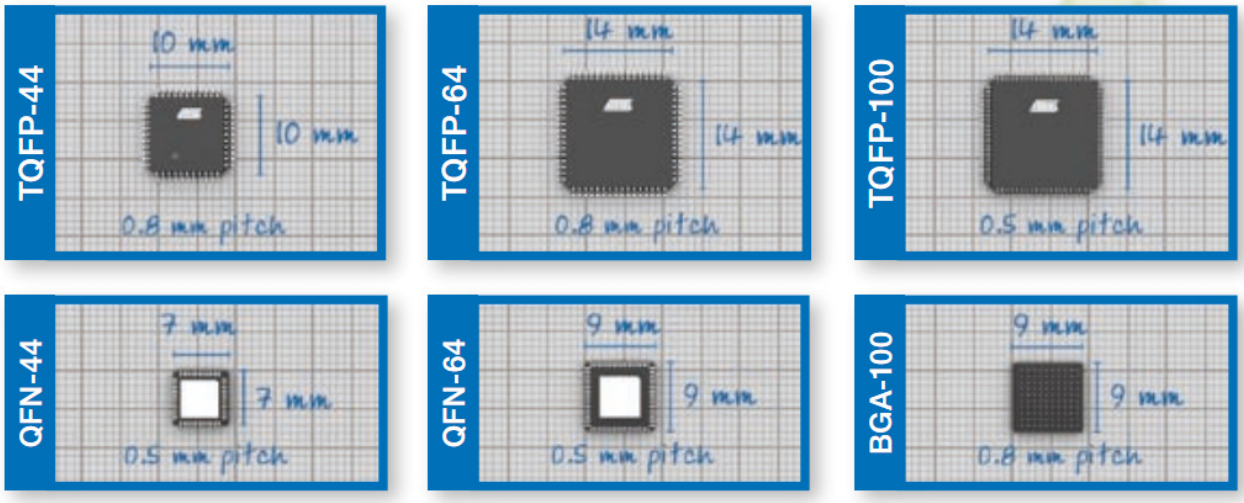
Pins/packages

AVR XMEGA is delivered in different package options, including fully green versions.



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